

WEST Search History

DATE: Saturday, February 12, 2005

Hide?	Set Name	Query	Hit Count
		<i>DB=EPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L16	MEMORY AND (PROGRAM\$ OR ADJUST\$) ADJ3 (DRIVER ADJ2 IMPEDANCE)	0
		<i>DB=TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L15	MEMORY AND (PROGRAM\$ OR ADJUST\$) ADJ3 (DRIVER ADJ2 IMPEDANCE)	0
<input type="checkbox"/>	L14	L13	0
		<i>DB=EPAB,JPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L13	MEMORY AND (PROGRAM\$ OR ADJUST\$) ADJ3 (DRIVER ADJ2 IMPEDANCE)	0
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L12	L8 AND ADJUST\$ ADJ3 (DRIVER ADJ2 IMPEDANCE)	0
<input type="checkbox"/>	L11	L7 AND PROGRAMMABLE IMPEDANCES	3
<input type="checkbox"/>	L10	L8 AND PROGRAM\$ ADJ5 IMPEDANCES	4
<input type="checkbox"/>	L9	L8 AND OUTPUT ADJ3 DRIVER ADJ3 IMPEDANCES	19
<input type="checkbox"/>	L8	L7 AND CLOCK ADJ2 (PRODUC\$ OR GENERAT\$)	618
<input type="checkbox"/>	L7	L6 AND DATA	931
<input type="checkbox"/>	L6	L5 AND CLOCK SIGNAL	933
<input type="checkbox"/>	L5	L3 AND OUTPUT ADJ2 DRIVER	1163
<input type="checkbox"/>	L4	L3 AND OUTPUT DRIVER	889
<input type="checkbox"/>	L3	L2 AND (MAIN OR SYSTEM) ADJ1 CLOCK	13258
<input type="checkbox"/>	L2	L1 AND (RAM OR RANDOM ACCESS)	262141
<input type="checkbox"/>	L1	MEMORY	701261

END OF SEARCH HISTORY

VIET Q. NGUYEN
 PRIMARY EXAMINER